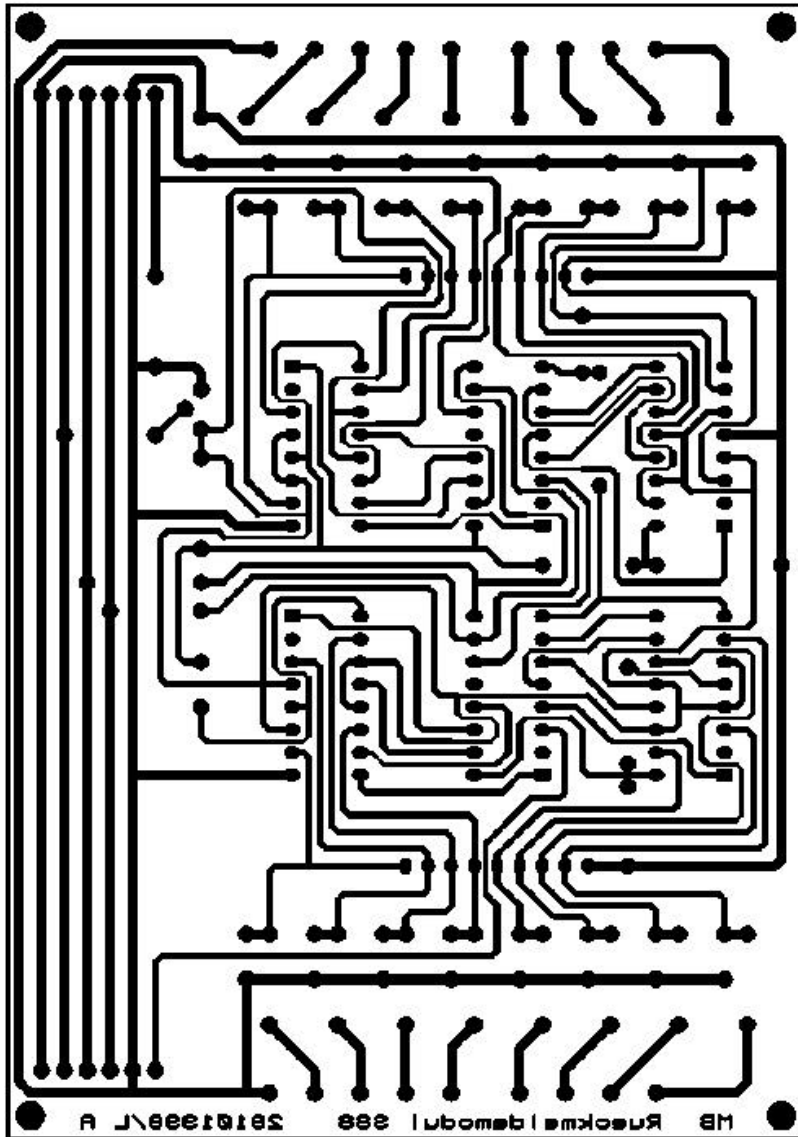


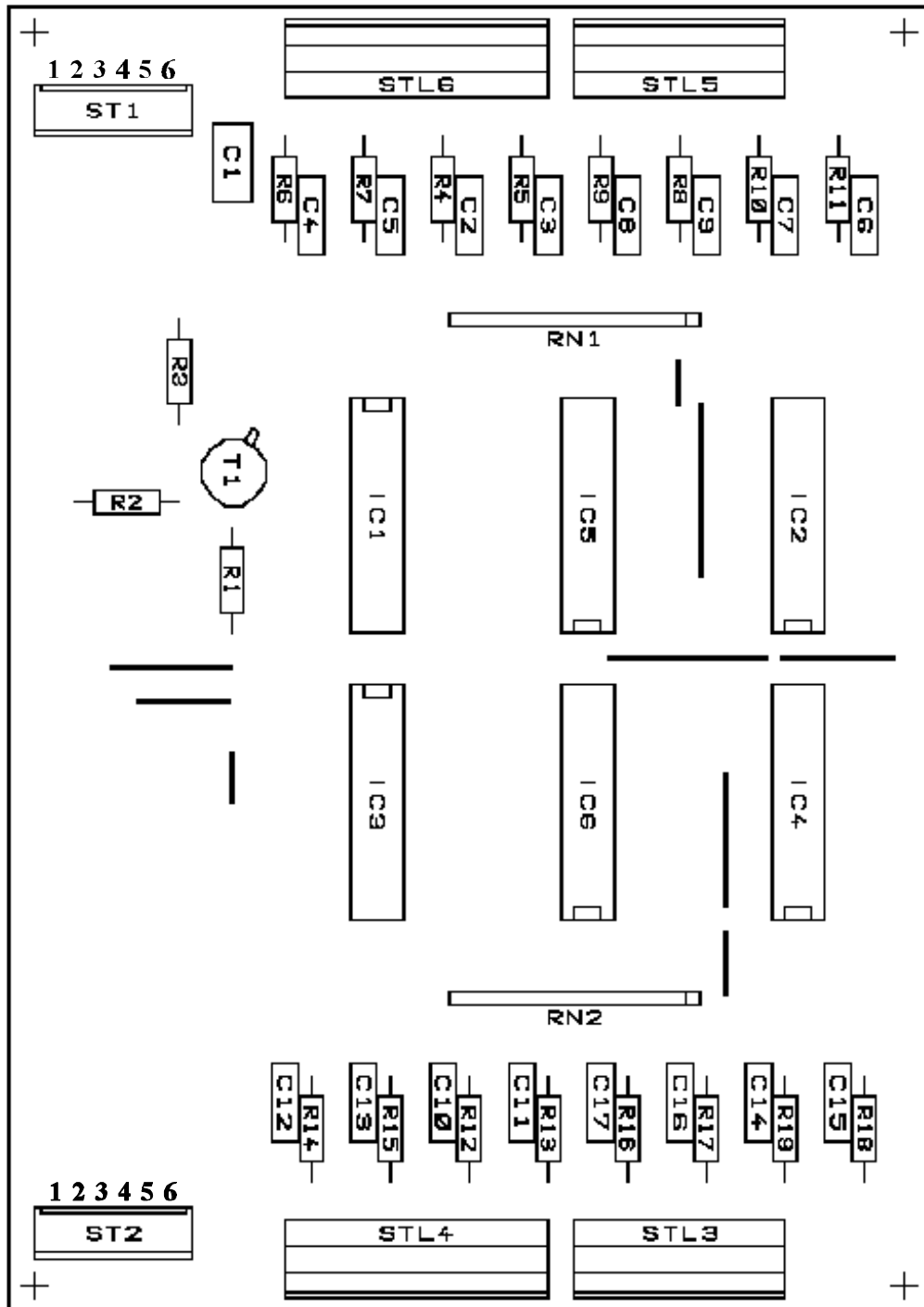
S88 Reply Module

The artwork might need to be touched up to ensure that there are no traces bridging into one another and it needs to be resized so the components will fit properly.



Part placement for S88 Reply Module

Pay attention to the orientation of the IC's 1-6, IC's 3-6 are upside down. The tab on the transistor indicates the emitter. All of the solid lines are bridges between two circuits and the left over leads from the mounted resistors and capacitors may be used for this.



S88 Part List

| Item | Quantity | Reference | Part |
|------|----------|---|-------------------|
| 1 | 2 | ST1,ST2 | STIFTLISTE 6POLIG |
| 2 | 18 | R1,R2,R4,R5,R6,R7,R8,R9, R10,R11,R12,R13,R14,R15, R16,R17,R18,R19 | 10K |
| 3 | 1 | R3 | 100K |
| 4 | 2 | RN2,RN1 | 100K |
| 5 | 16 | C2,C3,C4,C5,C6,C7,C8,C9, C10,C11,C12,C13,C14,C15, C16,C17 | 10nF |
| 6 | 1 | C1 | 100nF |
| 7 | 1 | T1 | TR-BCY59/IX |
| 8 | 2 | IC6,IC5 | IC-CMOS 4014 |
| 9 | 4 | IC1,IC2,IC3,IC4 | IC-CMOS 4044 |
| 10 | 2 | STL5,STL3 | MKDSN 1,5/4-5,08 |
| 11 | 2 | STL6,STL4 | MKDSN 1,5/5-5,08 |

S88 Pinouts

| Connector | Pin # | Function | Description |
|-----------|-------|----------|---|
| ST1/ST2 | 1 | +UB | +5V for reply modules |
| ST1/ST2 | 2 | Reset | Reset the reply modules for next readings |
| ST1/ST2 | 3 | Load | Load data into the reply modules |
| ST1/ST2 | 4 | Clock | Clock pulse to transfer data from reply modules to Memory or Interface |
| ST1/ST2 | 5 | Gnd | Ground |
| ST1 | 6 | Data in | Serial data from next S88 module connected in line |
| ST2 | 6 | Data out | Serial data to next S88 module connected in line or to Memory/Interface if it is the first reply module |